

FIG. 4

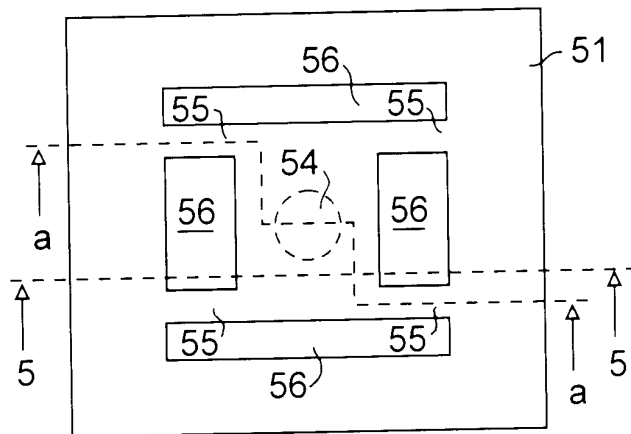


FIG. 5

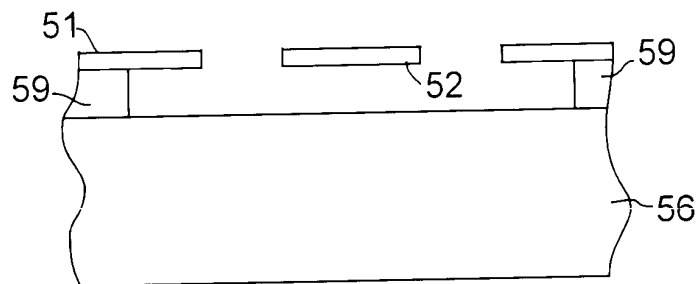


FIG. 6

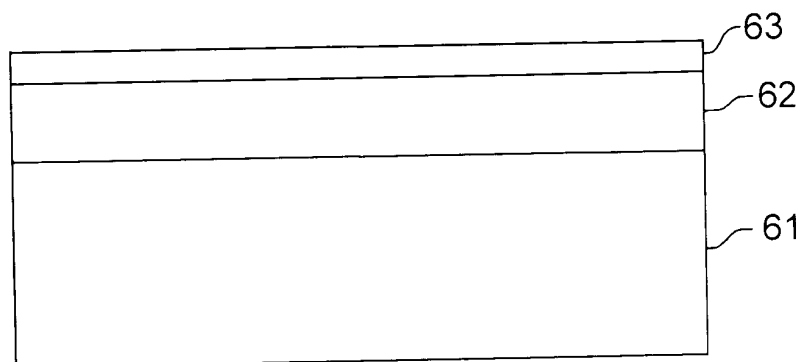


FIG. 7

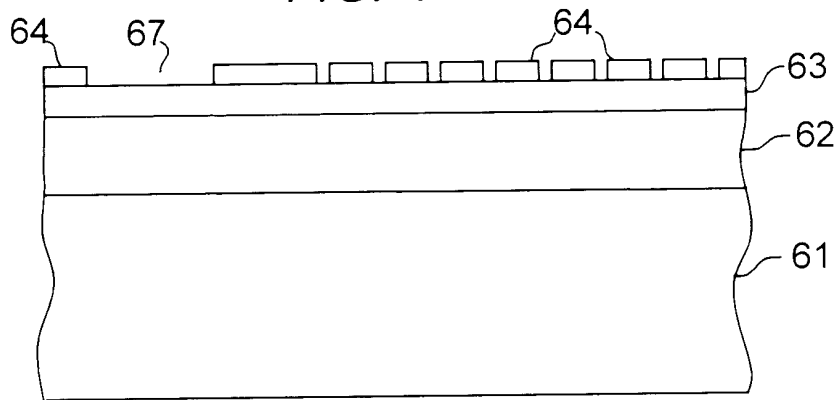


FIG. 8

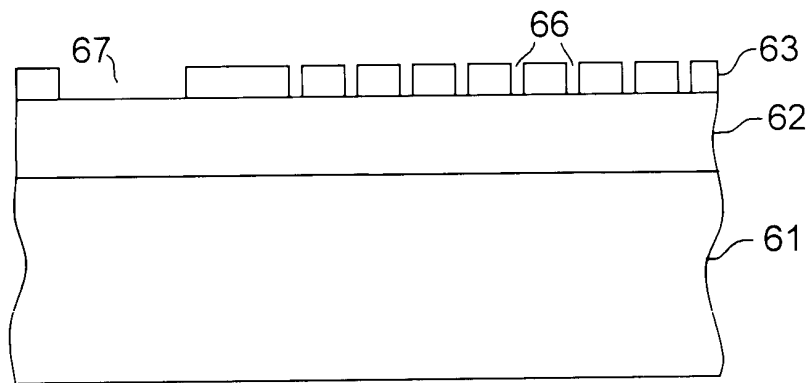
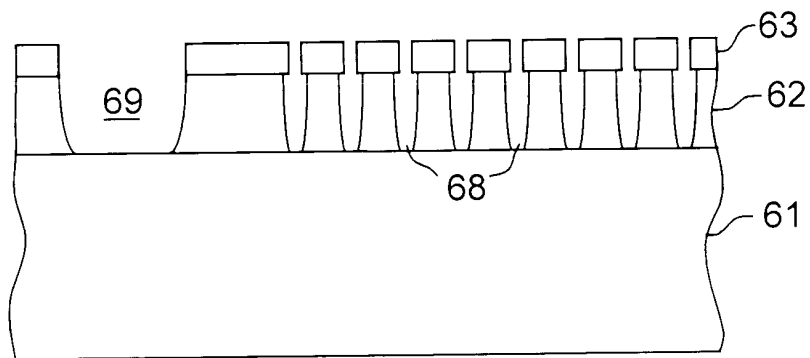


FIG. 9



A cross-sectional view of a semiconductor device. A substrate 61 is shown at the bottom. A layer 62 is formed on the substrate. A layer 63 is formed on the layer 62. A series of rectangular structures are formed on the layer 63. The structures are labeled 74 and 75. The label 74 points to the top surface of the structures, and the label 75 points to the side surface of the structures.

Figure 1B is a plan view of the first electrode 61. The electrode 61 has a circular region 76 on the left side and a series of rectangular regions 78 arranged in a row on the right side. The electrode 61 is connected to the first terminal 62.

FIG. 13

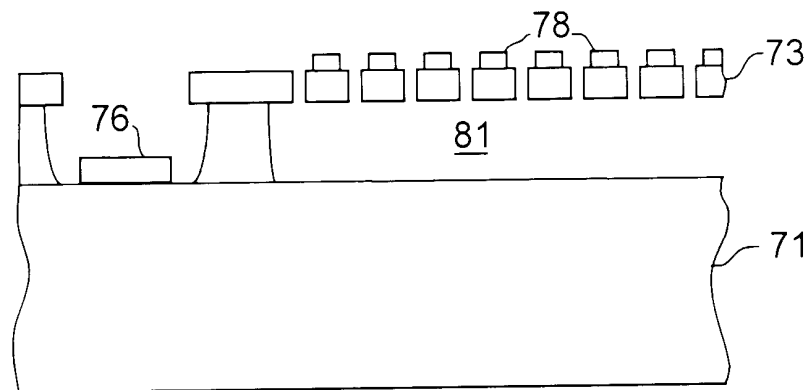


FIG. 14

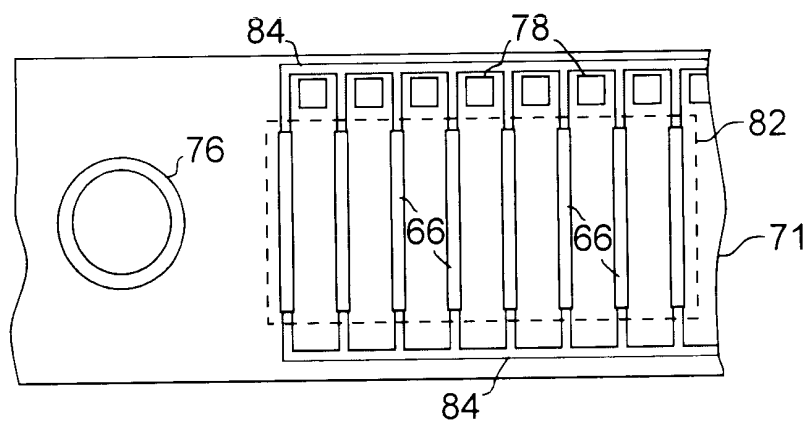


FIG. 15

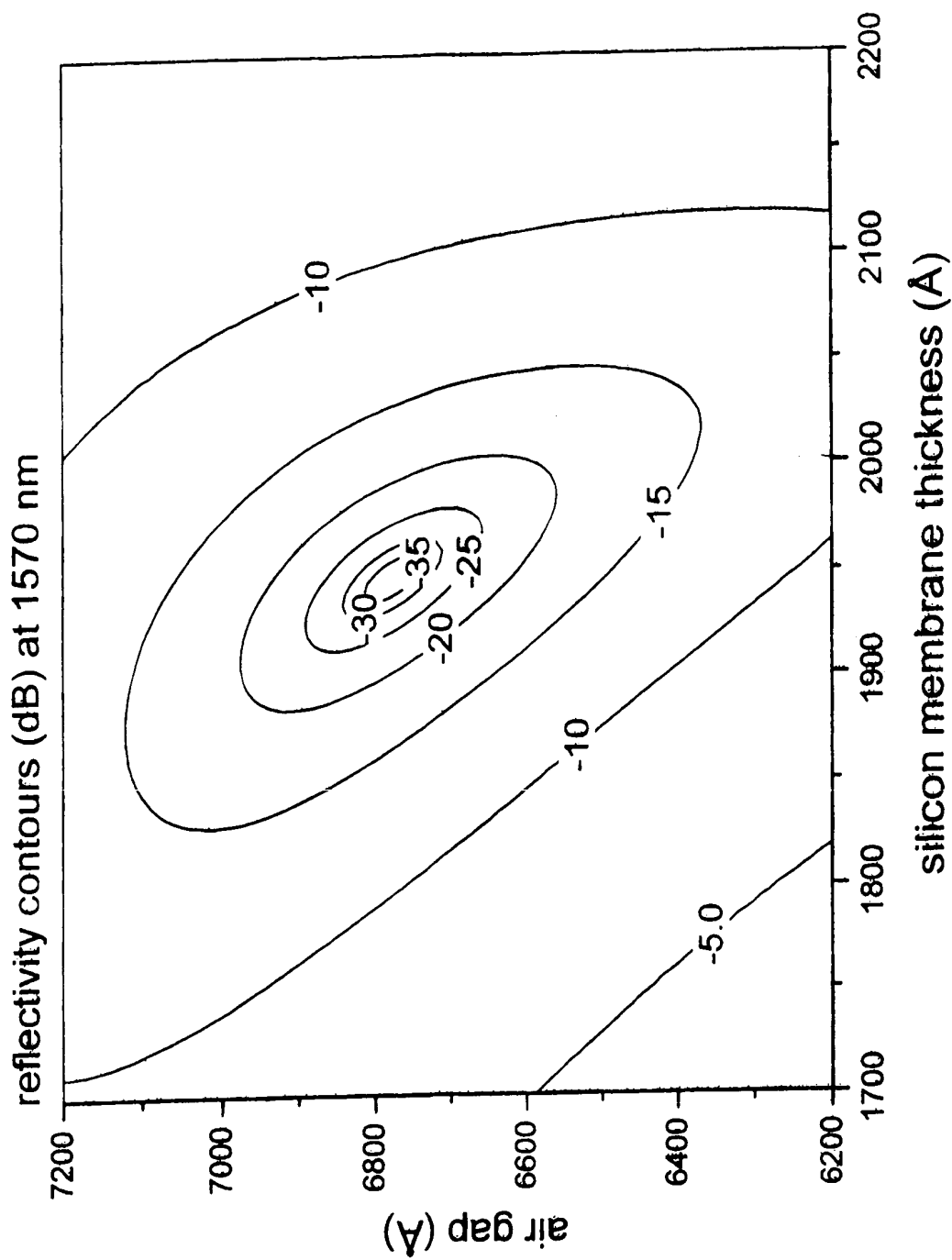


FIG. 16

